Precision Pulse Control

The PVX-4140 B pulse generator produces fast, high voltage wave forms, using an externally supplied voltage source up to 3,500V. Optimized for high impedance capacitive loads, the PVX-4140 B is well suited for driving extraction grids and deflection plates for electrostatic modulation of particle beams in time-of-flight mass spectrometers and accelerators. Its robust and versatile design also makes it well suited for pulsing or gating power tube grids, Pockels cells and Q Switches, acoustic transducers microchannel plates, photomultiplier tubes and image intensifiers. The exceptional pulse fidelity of the PVX-4140 B will optimize the performance of any system in which it is used.

System Operation

The PVX-4140 B has an output pulse of up to 3,500 volts with rise and fall times of 25ns or less, with very flat voltage pulses to DC into a capacitive load. Using the externally supplied high voltage source the PVX-4140 B can generate single-ended output pulses from ground to +3500V or from ground to −3500V, and can also generate pulses originating from a DC voltage offset from ground by using both \( V_{\text{LOW}} \) and \( V_{\text{HIGH}} \) power supply inputs. This offset can be from −3500V to +3500V, with a maximum power supply voltage differential of ≤3500V.

The PVX-4140 B requires a user-supplied TTL gate signal, a high voltage DC power supply and optional DC offset supply inputs. The output pulse width and frequency are controlled by the gate signal. The pulse output voltage is controlled by the amplitude of the input DC power supplies.

If the unit is enabled and the input gate is high, the \( V_{\text{HIGH}} \) supply is connected to the output. When the input gate is low, the \( V_{\text{LOW}} \) supply is connected to the output. Therefore the PVX-4140 B can be used to generate a negative-going pulse by logically inverting the input gate, so that the input gate is high until the unit is pulsed. When the input gate goes low, the \( V_{\text{LOW}} \) input supply is connected to the output, thereby generating a negative-going pulse.

The PVX-4140 B features front panel indicator LEDs to monitor the status of the pulse generator. Front panel instrument-quality voltage and current monitors provide a straightforward means to view the output voltage and current waveforms in real-time, eliminating the need for an external high voltage oscilloscope probe.

The pulse generator is a direct-coupled, air-cooled solid-state half-bridge (totem pole) design, offering equally fast pulse rise and fall times, low power dissipation, and virtually no over-shoot, under-shoot or ringing. It has overcurrent detection and shut-down circuitry to protect the pulse generator from potential damage due to arcs and shorts in the load or interconnect cable. All control and protection logic circuitry, support power, energy storage and output network are incorporated into the PVX-4140 B. It can be connected directly to the load, and does not require series or shunt resistors, impedance-matching networks between the pulser and the load, or additional energy storage (capacitor banks). All of this is taken care of within the PVX-4140 B.

Ordering Information

PVX-4140 B System

Remote Control

The PVX-4140 B has the capability to be operated through remote controls. Below is a diagram of the 15 pin DSUB connector on the rear panel.

The control pins (9, 10, 11, 12, 13) are internally pulled high. For remote operation, the REMOTE pin should be pulled low. This disables the Front Panel ENABLE switch to prevent local operation. The remote ENABLE, INTERLOCK, INVERT, and AUTO RESET inputs are always active, regardless of the state of this REMOTE signal. If the ENABLE signal is momentarily pulled low, the unit will be enabled, or a fault will be cleared, on the rising edge of that signal. If the INVERT pin is pulled low, the gate pulses going in to the unit will become inverted internally. Therefore, the output pulse will be inverted from that of the input gate.

The status pins (6, 7, 8, 14, 15) allow a remote status of the front panel LEDs and are open drain outputs and require an externally pull up resistor (100 kΩ) to a voltage source, less than 24 VDC.
Pulse Characteristics

Output Voltage Range 0 V to ±3500 V (V_{High} - V_{Low})
Controlled by power supply input voltages

Pulse Rise/Fall Time ≤ 25 ns (50 Ω load)

Pulse Width 60 ns to DC, Controlled by input gate

Frequency Range Single-shot to 30 kHz at 3500 V

Continuous output, Controlled by input gate

Max. Average Power 100 W (V_{High} + V_{Low})

Max. Duty Cycle Continuous

Droop <1%

Over/Undershoot <5%

Throughput Delay 120 ns Typical

Jitter <200 ps shot-to-shot

Input DC Voltage +V_{IN} (V_{High})

Absolute Max. Value +3500 V

Absolute Min. Value -3500 V

Relative Max. Value +3500 V over V_{Low} Voltage

Relative Min. Value +0 V over V_{Low} Voltage

Input DC Voltage -V_{IN} (V_{Low})

Absolute Max. Value +3500 V

Absolute Min. Value -3500 V

HV Input DC Connectors SHV, Rear Panel
(One each +Vin and -Vin)

Power Specifications

Voltage Specifications

<table>
<thead>
<tr>
<th>Voltage Requirements</th>
<th>100 VAC to 120 VAC ± 10%</th>
</tr>
</thead>
<tbody>
<tr>
<td>Line Frequency</td>
<td>50 Hz to 60 Hz</td>
</tr>
<tr>
<td>AC Connector Type</td>
<td>NEMA C-14</td>
</tr>
</tbody>
</table>

General

Size (HxWxD) 13.2 cm x 43.5 cm x 33.0 cm

Standard with rack ears 48.3 cm

Weight 8.2 kg

Operating Temperature 15 °C to 35 °C

Notes

Warranty: One year parts and labor on defects in materials and workmanship.

These specifications are measured driving a 50pF load connected with 6 feet of RG-59 cable, at 3500V output. However the PVX-4140 B can drive loads of a few picofarads to several hundred picofarads of capacitance, limited by its maximum power dissipation capability. At lower load capacitances and/or voltages less than 3500V, the PVX-4140 B can operate at continuous pulse recurrence frequencies up to 400kHz. The PVX-4140 B can also drive resistive or inductive loads, within limitations. Contact DEI for additional information and applications assistance.

(1) The power dissipated in the PVX-4140 B when driving a capacitive load is defined by the formula CV_{High}F, where C is the total load capacitance, including the capacitance of the load, interconnect cable, and the internal capacitance of the PVX-4140 B, V is the pulse voltage, and F is the pulse repetition frequency (or the total pulses per second). (For these calculations, the internal capacitance of the PVX-4140 B is 120pF, and RG-59 cable is 21.5pf/foot.) Given the maximum dissipation of 100W, the maximum load capacitance, frequency and/or voltage at which the PVX-4140 B can operate can be approximated using this formula. This formula also approximates the high voltage power supply requirements needed to drive a given load at a specific voltage and frequency. This formula is not applicable when driving resistive or inductive loads.

Specifications subject to change without notice.

For more information: 970.493.1901 or sales@directedenergy.com

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